

FIG. 1

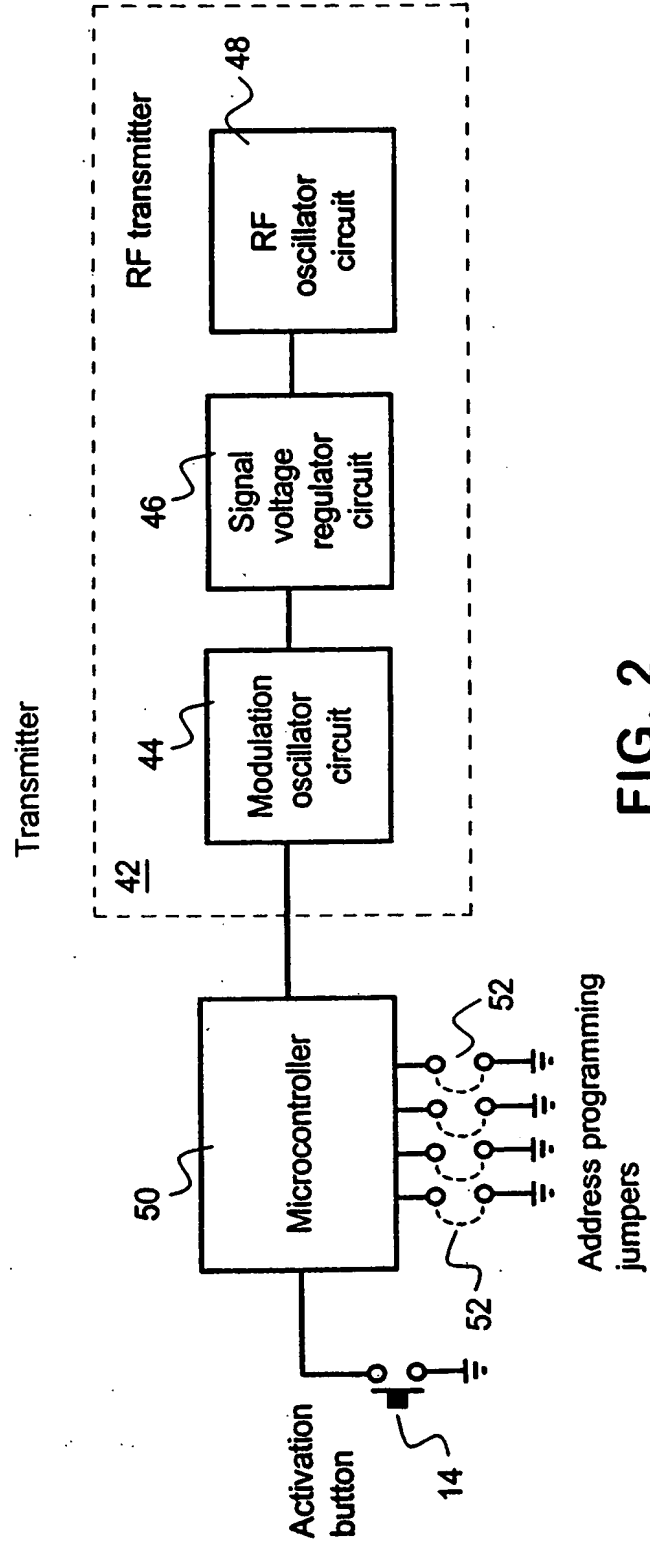


FIG. 2

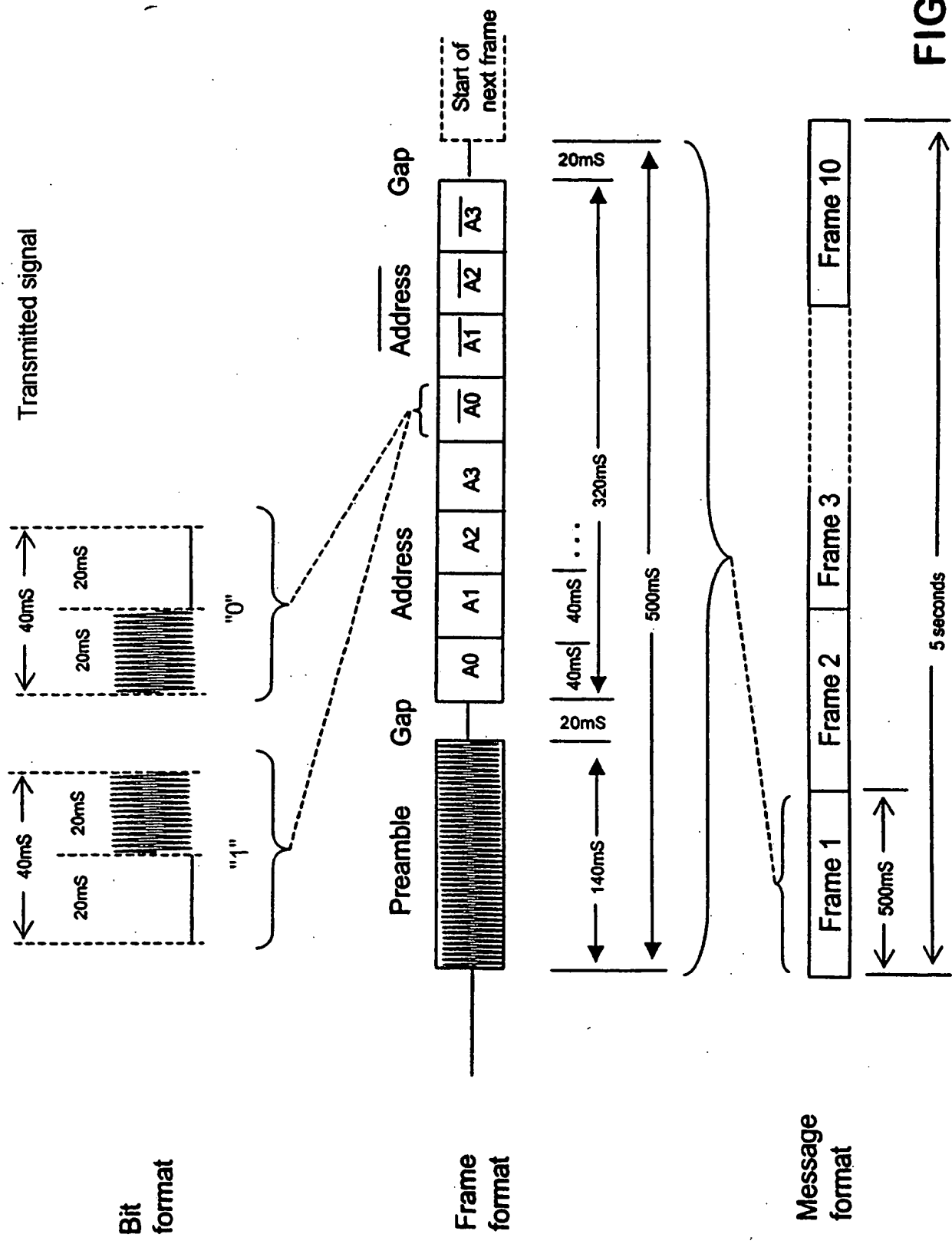


FIG. 3

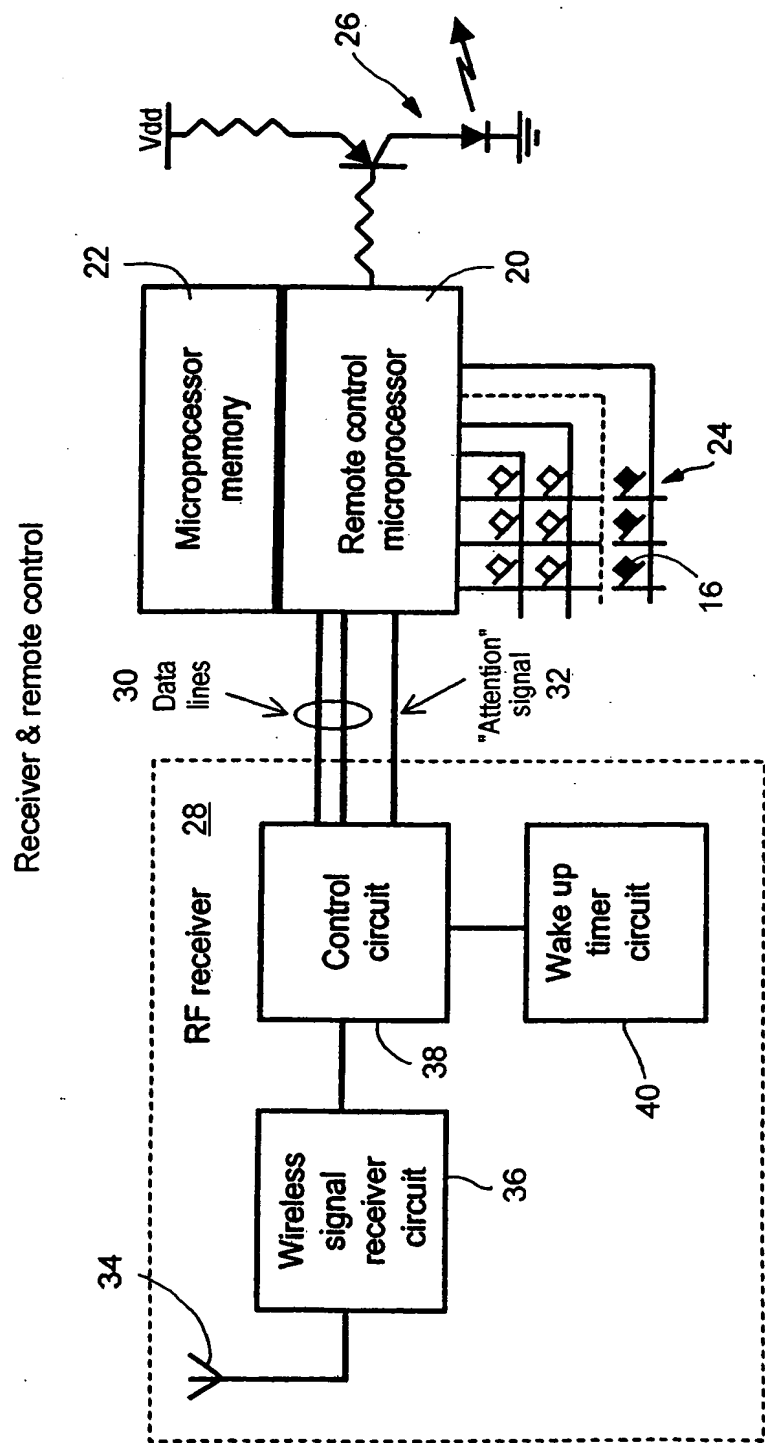
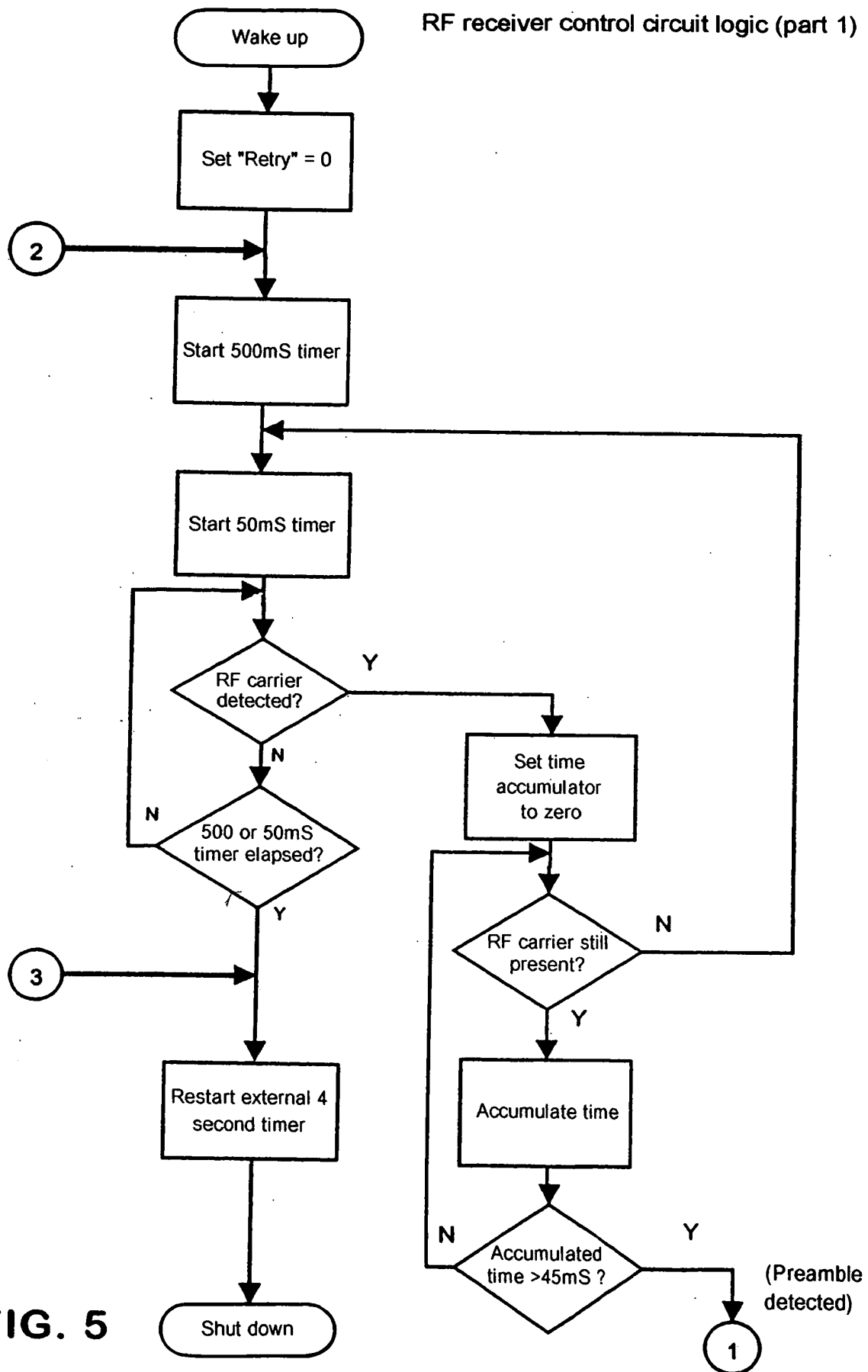
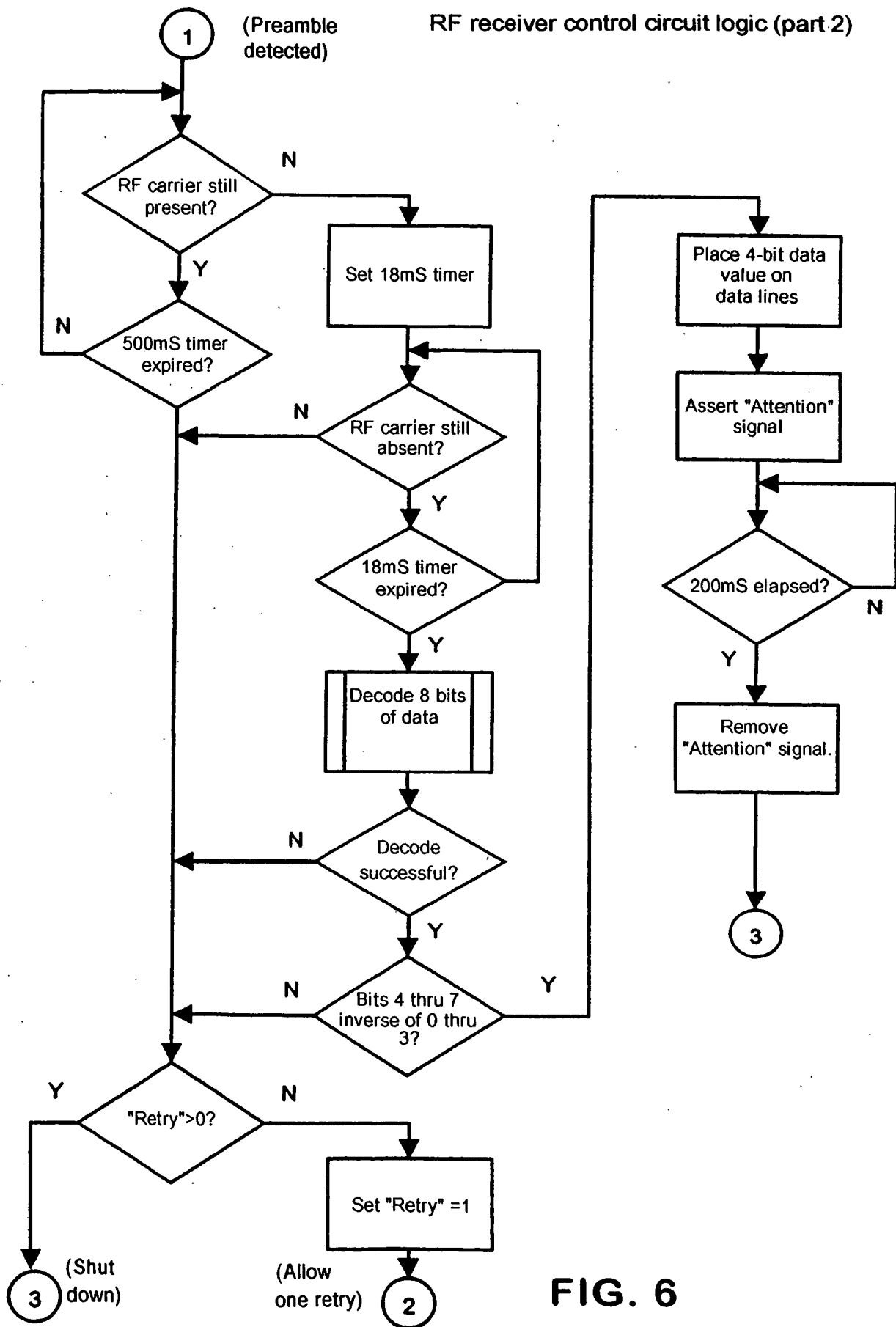


FIG. 4

# RF receiver control circuit logic (part 1)



**FIG. 5**



**FIG. 6**

Remote control processor logic

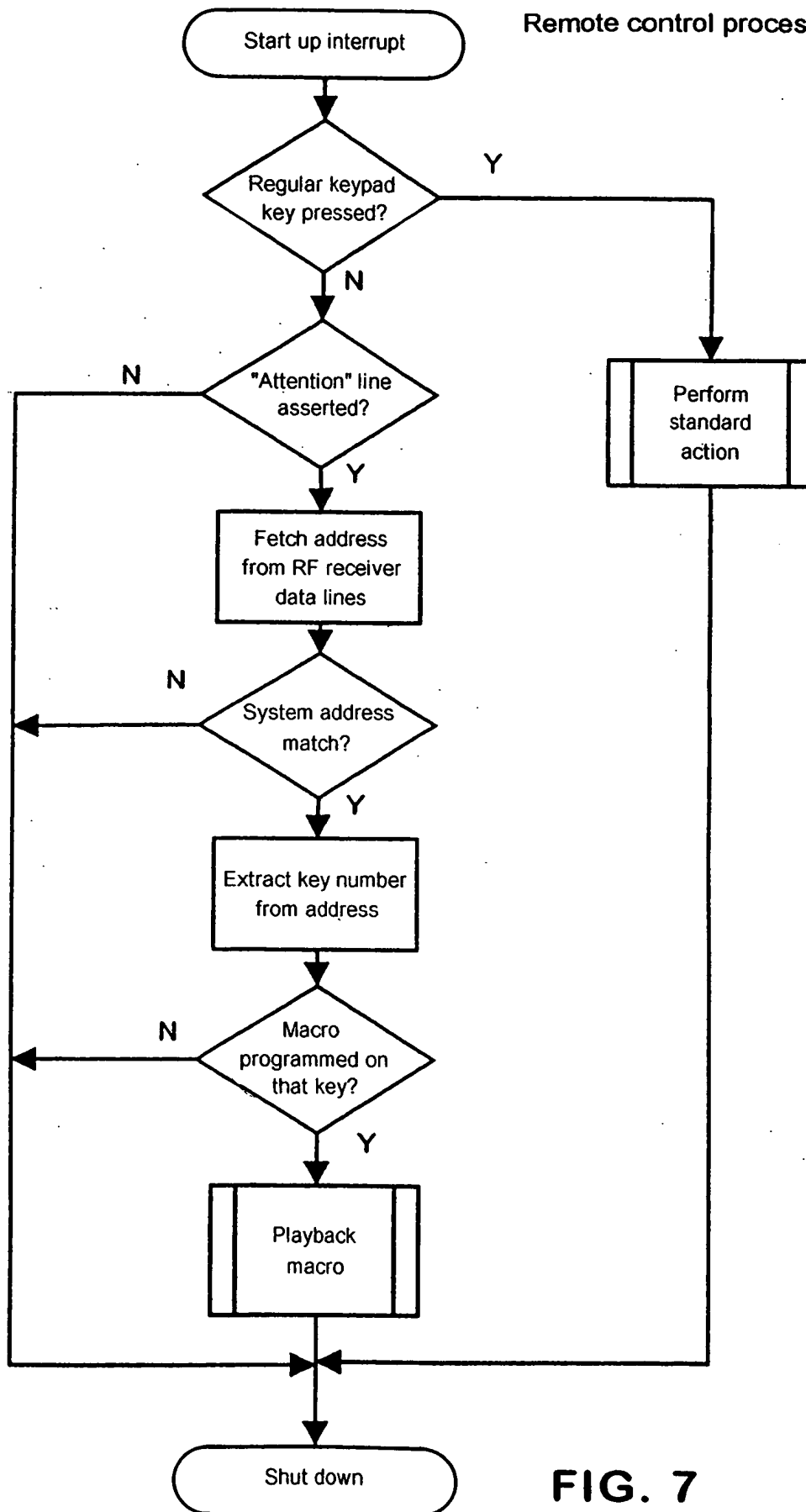


FIG. 7